

## SLM755 Q ‡?ô IB\$ &gt; \_V1.01

|          |                                 |
|----------|---------------------------------|
| × · & =0 | SLM755 Q ‡?ô IB\$ >             |
| ×(x\ '   | V1.01                           |
| 3 j ´    | \$! c 25¾ I ª7- ° _6Ñ - 9L€ œ ( |
| 3 O      | 2016                            |

# 1. × ñ ²F

SLM755 \_0!n ¯‘µ^j 8GB(¬é 16GB),´~f? J Q LTE ¢7-FJ Q ‡ È" Q ‡ F2  
+X ¾TD-LTE/FDD-LTE/WCDMA/EVDO/TD-SCDMA/CDMA/GSM J/ý5•5 f?,´íV  
¢7- 4iFJ Q ‡ Ä SLM755 G÷+XAndroid5.1 ýÆ3+5 È \_1,´ Ö •FO)· Ö  
TDD-LTE: 117/30Mbps  
FDD-LTE: 150Mbps/50Mbps  
WCDMA Eî DC HSPA+: 42Mbps/5.76Mbps  
EVDO Eî EVDO RevA: 3.1Mbps/1.8Mbps  
TD-SCDMA Eî HSPA: 4.2Mbps/2.2Mbps  
CDMA1x: 153.6kbps/153.6kbps  
GSM Eî EDGE: 236.8kbps/236.8kbps

SLM755 X ËQ FO íV ž Ö •,´ & È ËB N# Ä ÄFJAß2o Ä WiFi ÄBT ¼GPS  
Ï7- È ¯# Ä+X ¾?öNÁ- × Ä ¹Lb ÄE–E-Aî 7 Ä ¢7- £ { 1 4ø1 1y × ñ Ä

## 2. (© W G>~

|                           |                                                              |                                                                                     |
|---------------------------|--------------------------------------------------------------|-------------------------------------------------------------------------------------|
| Camera<br>Ä }¼ > ü tÿ d Å | Interface: main: MIPI CSI0 4-lanes; front: MIPI CSI1 2-lanes |                                                                                     |
|                           | tÿ d Pixel: Max: }5Ž 5M/ >5Ž 13M                             |                                                                                     |
|                           | Video decode                                                 | 1080p 30 fps: H264/ MP4/ WMV9/ VC1/ DivX/ VP8<br>HEVC (H.265) 720p 30fps in SW      |
|                           | Video encode                                                 | 1080p 30 fps: H264/ H263/ MP4/ VP8                                                  |
| EÄ •Aî 7                  | 9K^ Ä 0 £ jK^ ÄN#Gÿ + ÄN#Gÿ Ä                                |                                                                                     |
|                           | +e é ? TP                                                    |                                                                                     |
| Reset                     | _ 1.œ = }                                                    |                                                                                     |
| Ä+X Õ                     | Õ =0                                                         | k?± î7- ÿF                                                                          |
|                           | VBAT                                                         | Q ‡+e\$ÄEÄ • È3.3V 4.2V È 70 l 3.8V                                                 |
|                           | SDIO *1                                                      | TF Card È 0 W _ 1 32GB                                                              |
|                           | USB                                                          | _ 1 OTG<br>FORCE_USB_BOOTÄ : ù j f USB E , È+X ¾3W<br>U ;E- Ä                       |
|                           | UART*2                                                       | 04ô 4 4ï uart È 04ô 2 4ï uart                                                       |
|                           | I2C*4                                                        | For sensors/TP/others                                                               |
|                           | SPI*1                                                        | Master only                                                                         |
|                           | ADC*2                                                        | _ 1                                                                                 |
|                           | u+e î7-                                                      | _ 1 FG u+e8ß(w                                                                      |
|                           | PœEî                                                         | _ 1                                                                                 |
|                           | GPIO                                                         | 24                                                                                  |
|                           | VCOIN                                                        | Î & &JÎ > 7+e"•                                                                     |
|                           | 4NÁ Õ                                                        | Fp/„                                                                                |
|                           | Audio                                                        | 1 D ...1 MIC(MŽ.uMIC),1 D 6c j MIC<br>1 D } · Ä V Ĭ n Å<br>1 D \1,<br>1 D 0û f 6c j |

### 3. SLM755 Õ

SLM755 210pin

| pin# | pin name | Pad characteristics | Functional description |
|------|----------|---------------------|------------------------|
| 1    | VBAT     | PI,PO               | Battery                |
| 2    | VBAT     | PI,PO               | Battery                |
| 3    | GND      | GND                 | GND                    |

|    |                 |               |                                               |
|----|-----------------|---------------|-----------------------------------------------|
| 36 | UART_CTS        | B-PD:nppukp   | Configurable I/O UARTor I2C SDA               |
| 37 | UART_RTS        | B;B-PD:nppukp | Configurable I/O UARTor I2C SCL               |
| 38 | VREG_L11_SDC    | PO            | PMIC output 2.95V                             |
| 39 | SDC2_SDCARD_CLK | BH-NP:pdpukp  | Secure digital controller 2 clock             |
| 40 | SDC2_SDCARD_CMD | BH-PD:nppukp  | Secure digital controller 2 command           |
| 41 | SDC2_SDCARD_D0  | BH-PD:nppukp  | Secure digital controller 2 data bit 0        |
| 42 | SDC2_SDCARD_D1  | BH-PD:nppukp  | Secure digital controller 2 data bit 1        |
| 43 | SDC2_SDCARD_D2  | BH-PD:nppukp  | Secure digital controller 2 data bit 2        |
| 44 | SDC2_SDCARD_D3  | BH-PD:nppukp  | Secure digital controller 2 data bit 3        |
| 45 | SDC2_SDCARD_DET | B-PD:nppukp   | ConfigurDC 1/P ÆMCID 189 ¶T1 8.04 Tf1 0 75896 |

76 GND

|     |                |             |                             |
|-----|----------------|-------------|-----------------------------|
| 117 | GPIO10_SPI_CS  | B-PD:nppukp | Configurable I/O SPI or I2C |
| 118 | GPIO9_SPI_MISO | B-PD:nppukp | Configurable I/O SPI        |
| 119 | GPIO8_SPI_MOSI | B-PD:nppukp | Configurable I/O SPI        |
| 120 | GND            | GND         | GND                         |
| 121 | RF_GPS         | AI          | RF signal-GPS ANT           |
| 122 | GND            | GND         | GND                         |
| 123 | GPIO31         | B-PD:nppukp | Configurable I/O SPI or I2C |
| 124 | GPIO69         | B-PD:nppukp | Configurable I/O SPI or I2C |





# 4. Q ‡ 2D5 ´.



